10/518602 Rec'd POT/PTO 17 DEC 200

Docket No.: U1927.0014

(PATENT)

Express Mail Label No. EV 262 741 484 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Yuji Ando et al.

Application No.: Not Yet Assigned

Confirmation No.: N/A

Filed: Concurrently Herewith

Art Unit: N/A

For:

SEMICONDUCTOR DEVICE HAVING

SCHOTTKY JUNCTION ELECTRODE

Examiner: Not Yet Assigned

INFORMATION DISCLOSURE STATEMENT (IDS)

MS PCT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

Timing of Filing of the Information Disclosure Statement:

| \boxtimes | This IDS is being filed before the First Office Action ¹ . |
|-------------|---|
| | This IDS is being filed after the issuance of the First Office Action but before the issuance of a Final Office Action ² . |

¹ The IDS should, where possible, include a certification under 37 C.F.R. §1.97(e).

² The IDS *must* include *either* a certification under 37 C.F.R. §1.97(e) or the fee set forth in 37 C.F.R. §1.17(p).

| | This IDS is being filed after the issuance of a Final Office Action but before the payment of the Final Fee ³ . |
|-------------|--|
| <u>Cert</u> | ifications: |
| If ch | ecked, the undersigned makes the following statement(s): |
| | Statement under 37 CFR § 1.97(e): |
| | Each item of information contained in this information disclosure statement was |
| | first cited in any communication from a foreign patent office in a counterpart |
| | foreign application not more than three months prior to the filing of this |
| | information disclosure statement; or |
| | No item of information contained in this information disclosure statement was cited |
| | in a communication from a foreign patent office in a counterpart foreign |
| | application, and, to the knowledge of the undersigned after making reasonable |
| | inquiry, no item of information contained in this information disclosure statement |
| | was known to any individual designated in § 1.56(c) more than three months prior |
| | to the filing of the information disclosure statement. |
| | Statement Under 37 C.F.R. § 1.704(d): |
| | Each item of information contained in this information disclosure statement was |
| | cited in a communication from a foreign patent office in a counterpart application |
| | less than thirty days prior to the filing of this information disclosure statement. |
| Fee I | Required by 37 C.F.R. § 1.97(c)(2) or 1.97(d)(2): |
| | If checked, the fee of \$180.00 set forth in 37 C.F.R. \$1.17(p) is attached. |
| | 2 |
| | The IDS must include both a certification under 37 C.F.R. \$1.97(e) and the fee set forth in 37 |

C.F.R. \$1.17(p).

Copies of Information:

In accordance with 37 C.F.R. §1.98(a), the following are enclosed: M A legible copy⁴ of each document (or relevant portion thereof) is cited in the attached PTO/SB/08. \boxtimes With respect to any information which is not in English, a concise explanation of the relevance, as it is presently understood by the individual designated in § 1.56(c) most knowledgeable about the content of the information, is attached. This concise explanation is provided by way of: \boxtimes An abstract of the relevant portions of the non-English language information⁵; A statement explaining the relevant portions of the non-English language information; X A copy [and, where not in the English language, a translation] of at least the relevant portion(s)⁶ of the communication from a foreign patent office in a counterpart foreign application in which the information was cited; or This information is contained in the specification of the present application.

⁴ A legible copy of the document is not required if (1) the information was previously cited by, or submitted to, the Office and considered by the Office in a prior U.S. application to which this application claims priority, provided that the prior application is properly identified in this IDS, and (2) the IDS submitted in the earlier application complies with 37 C.F.R. § 1.98(a) - (c). This exception does not apply to information cited in an International Application.

⁵ 37 C.F.R. §1.98(a)(3)(ii) requires that an English language translation be provided when a translation of the document, or portion thereof, "is within the possession, custody or control of, or is readily available to any individual designated in 37 C.F.R. § 1.56(c)."

⁶ The relevant portion is that portion which indicates the degree of relevance found by the foreign patent office. This may be an explanation of which portion of the of the reference is particularly relevant, to which claims it applies, or merely an "X", "Y", or "A" indication on a search report. MPEP \$609 III A(3).

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In accordance with 37 C.F.R. 1.98(d), copies of the cited documents are not enclosed as they were provided in application Serial No. , filed , which the present application relies upon for an earlier effective filing date under 35 U.S.C. 120.

Materiality:

Whether or not the information and references disclosed in this Information Disclosure Statement is "material" pursuant to 37 CFR 1.56, this submission is not intended to constitute an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists.

It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

In the event the actual fee is inadvertently not enclosed or if any additional fee during the prosecution of this application is not paid, the Patent Office is authorized to charge the underpayment to Deposit Account No. 50-2215.

Dated: December 17, 2004

Respegtfully submitted

Michael J. Scheer

Registration No.: 34,425

DICKSTEIN SHAPIRO MORIN &

OSHINSKY LLP

1177 Avenue of the Americas

New York, New York 10036-2714

(212) 835-1400

Attorney for Applicant

MJS/da

PTO/SB/08a/b (08-03)

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| Substitute for form 1449A/B/PTO | | | | Application Number | Not Yet Assigned | |
| INF | ORMATI | ON DISC | LOSURE | Filing Date | Concurrently Herewith | |
| STATEMENT BY APPLICANT | | | | First Named Inventor | Yuji Ando | |
| (Use as many sheets as necessary) | | | 2107 | Art Unit | N/A | |
| | | | essary) | Examiner Name | Not Yet Assigned | |
| Sheet | 1 | of | 1 | Attorney Docket Number | U1927.0014 | |

| U.S. PATENT DOCUMENTS | | | | | | |
|-----------------------|--------------|--|--------------------------------|--|---|--|
| Examiner Initials* | Cite No.1 | Document Number Number-Kind Code ² (if known) | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear | |
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| Examiner | Cite No.1 | Foreign Patent Document | | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear | |
| Initials* | | Country Code ³ -Number ⁴ -Kind Code ⁵ (if known) | | | | Ø |
| | BA | JP-2001-156081 | 06-08-2001 | MATSUSHITA ELECTRONICS INDUSTRY CORP | | |
| | ВВ | JP-2000-277724 | | NAGOYA KOGYO UNIV & NIPPON SANSO CORP | | Ø |
| | вс | JP-11-354817 | 12-24-1999 | FURUKAWA ELECTRIC CO LTD | | × |

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| | NON PATENT LITERATURE DOCUMENTS | | | | | |
|---------------|---------------------------------|---|----------------|--|--|--|
| Examiner Cite | | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² | | | |
| | CA | T. Egawa, et al.; "Recessed gate AlGaN/GaN modulation-doped field-effect transistors on sapphire": Appl. Phys. Lett., January 3, 2000; Vol. 76(1); pp. 121 - 123 | | | | |
| | СВ | Egawa, Takashi, et al.; "Characteristics of a GaN Metal Semiconductor Field-Effect Transistor Grown on a Sapphire Substrate by Metalorganic Chemical Vapor Deposition"; Jpn. J. Appl. Phys.; April 1999; Vol. 38; Part 1; No. 4B; pp. 2630 - 2633 | | | | |

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| Examiner | | onsidered | |
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